

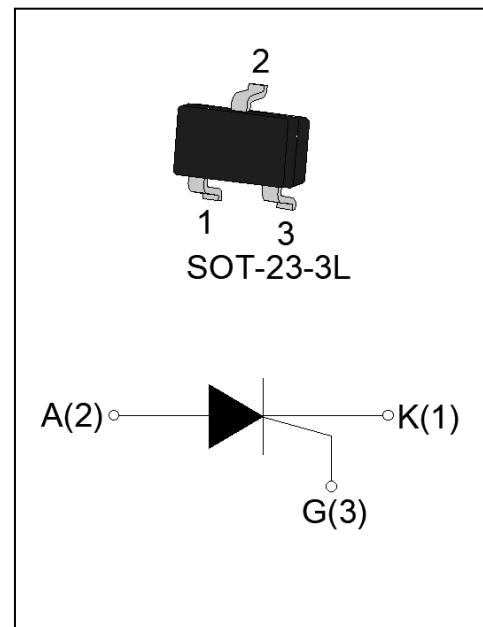


100-8 0.8A Sensitive SCR

Rev.A.1.0

DESCRIPTION:

The 100-8 SCR provides high dV/dt rate with strong resistance to electromagnetic interface. It is especially recommended for use on residual current circuit breaker, straight hair, igniter etc. Package SOT-23-3L is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	0.8	A
V_{DRM} / V_{RRM}	800	V
I_{GT}	≤ 200	μA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-125 ^①	°C
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
Average on-state current ($T_c \leq 63^\circ C$)	$I_{T(AV)}$	0.5	A
RMS on-state current ($T_c \leq 63^\circ C$)	$I_{T(RMS)}$	0.8	A
Non repetitive surge peak on-state current ($t_p=10ms, T_j=25^\circ C$)	I_{TSM}	8	A
Non repetitive surge peak on-state current ($t_p=8.3ms, T_j=25^\circ C$)		9	
I^2t value for fusing ($t_p=10ms, T_j=25^\circ C$)	I^2t	0.32	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}, f=100Hz, T_j=125^\circ C$)	dI/dt	50	$A/\mu s$
Peak gate current ($t_p=20\mu s, T_j=125^\circ C$)	I_{GM}	1	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	0.1	W

Peak gate power	P_{GM}	2	W
Peak pulse voltage ($T_j=25^\circ C$; non-repetitive, off-state; FIG.8)	V_{pp}	1	kV

NOTE 1: When we parallel connect a $\leq 1K\Omega$ resistor between Gate and Cathode, the T_j can reach $125^\circ C$; if without this resistor, the T_j only can reach $110^\circ C$.

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ C$ unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12V R_L=33\Omega$	-	50	200	μA
V_{GT}		-	0.6	0.8	V
V_{GD}	$V_D=V_{DRM} T_j=125^\circ C$	0.2	-	-	V
I_L	$I_G=1.2 I_{GT}$	-	-	4	mA
I_H	$I_T=0.05A$	-	-	3	mA
dV/dt	$V_D=540V T_j=125^\circ C R_{GK}=1K\Omega$	200	-	-	V/ μs
	$V_D=540V T_j=125^\circ C R_{GK}=220\Omega$	500	-	-	
t_{on}	$I_G=10mA I_A=20mA I_R=2mA T_j=25^\circ C$	-	2	-	μs
t_{off}		-	50	-	μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_T=1A t_p=380\mu s$	$T_j=25^\circ C$	1.35	V
V_{TO}	Threshold voltage	$T_j=125^\circ C$	0.93	V
R_D	Dynamic Resistance	$T_j=125^\circ C$	0.34	Ω
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25^\circ C$	2	μA
I_{RRM}		$T_j=125^\circ C$	0.2	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (DC)	60	$^\circ C/W$
$R_{th(j-a)}$	junction to ambient (DC)	120	$^\circ C/W$

MARKING

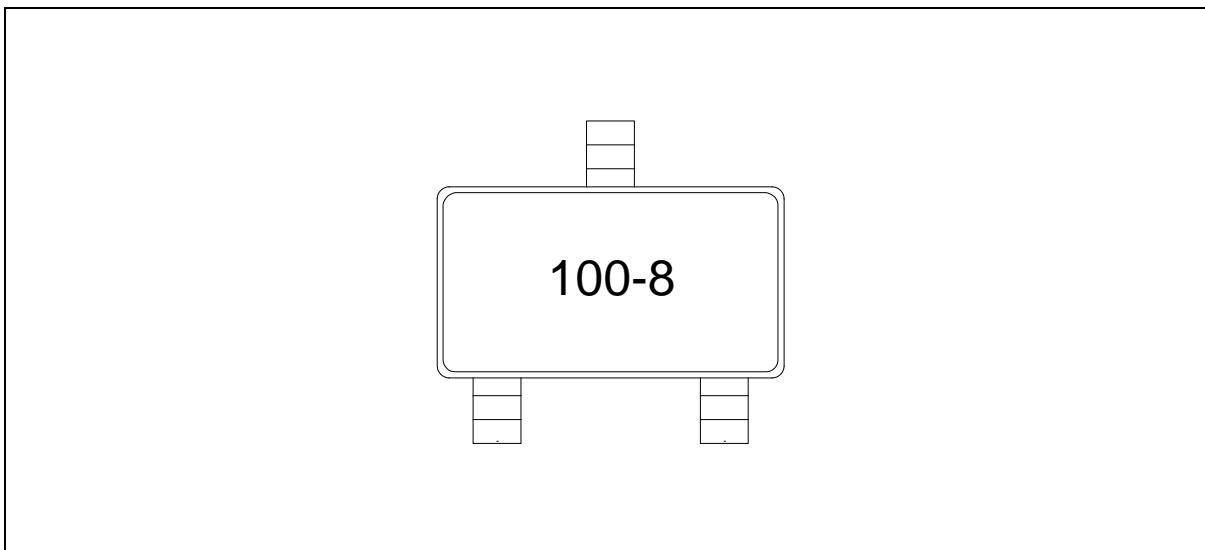


FIG.1 Maximum power dissipation versus RMS on-state current

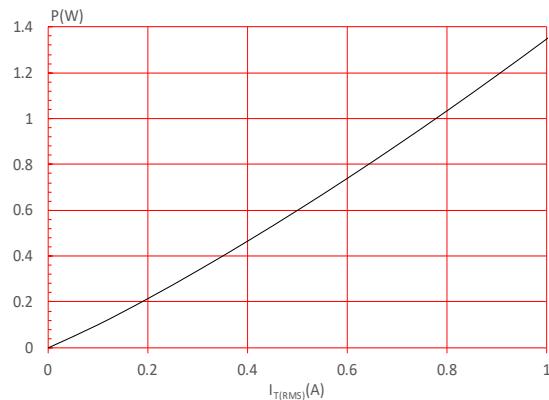


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

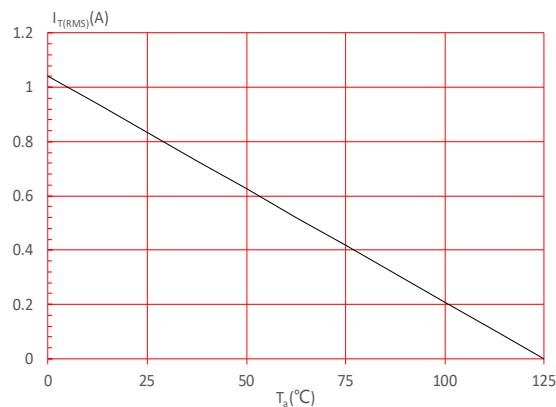


FIG.2: RMS on-state current versus case temperature

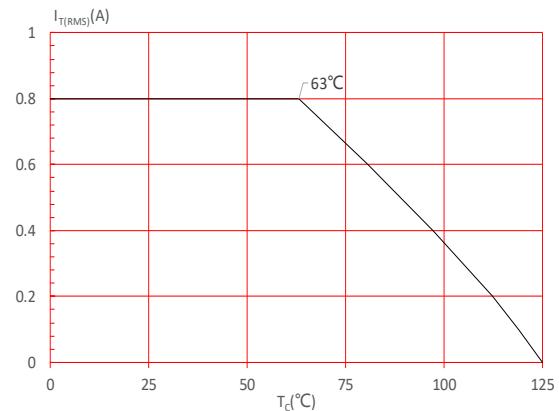


FIG.4: Surge peak on-state current versus number of cycles

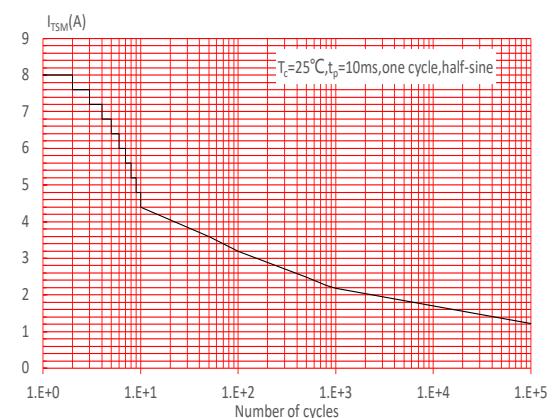


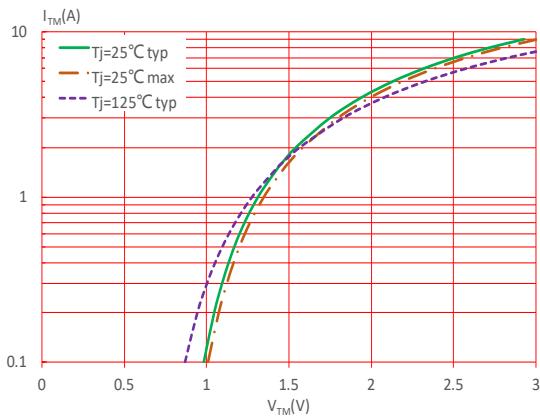
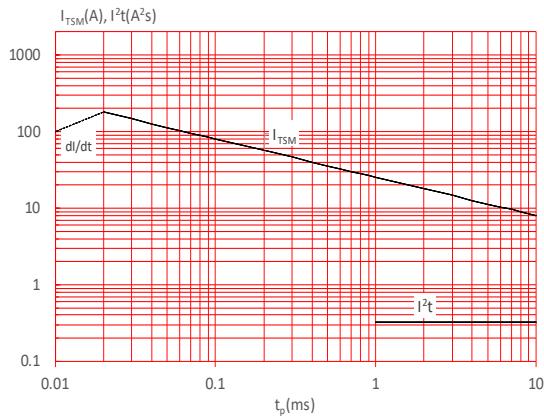
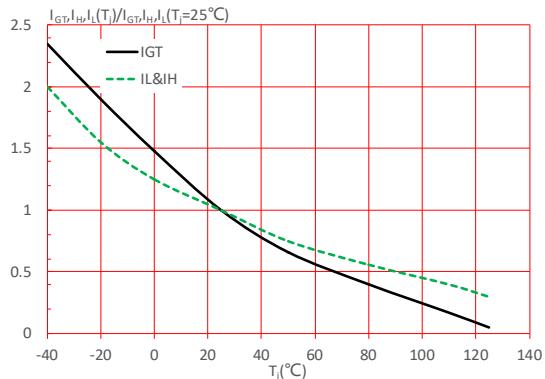
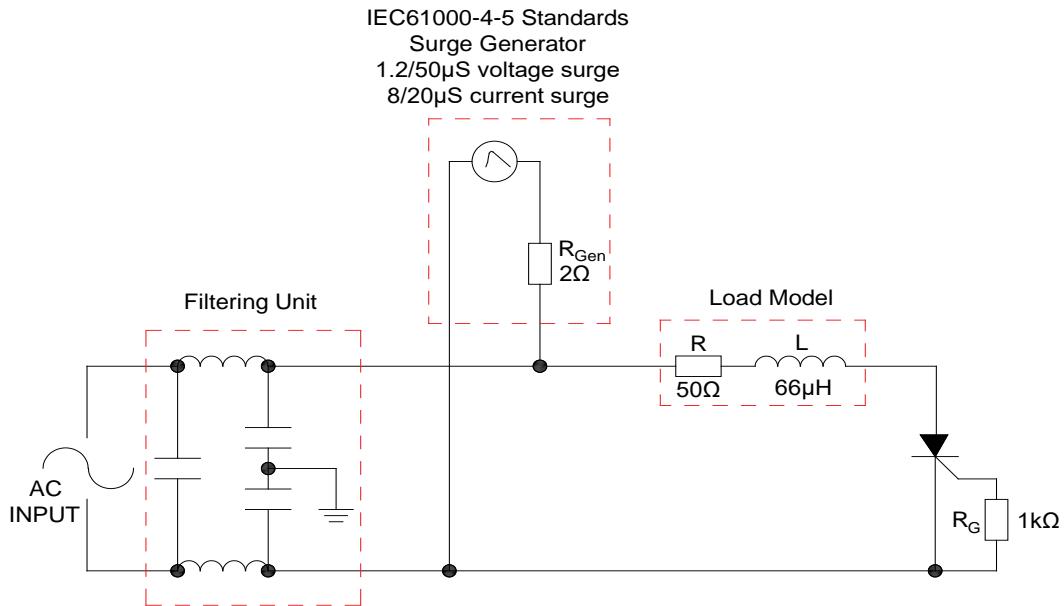
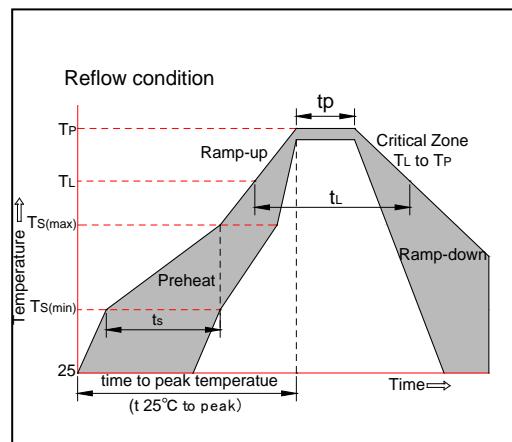
FIG.5: On-state characteristics**FIG.6:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$, and corresponding value of I^2t ($dl/dt < 50\text{A}/\mu\text{s}$)**FIG.7:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)
Pre Heat	-Temperature Min ($T_{s(\min)}$)	+150°C
	-Temperature Max ($T_{s(\max)}$)	+200°C
	-Time (Min to Max) (t_s)	60-180 secs.
Average ramp up rate (Liquidus Temp (T_L) to peak)		3°C/sec. Max
$T_{s(\max)}$ to T_L - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature(T_L) (Liquidus)	+217°C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_p)		20-40secs.
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp (T_p)		8 min. Max
Do not exceed		+260°C



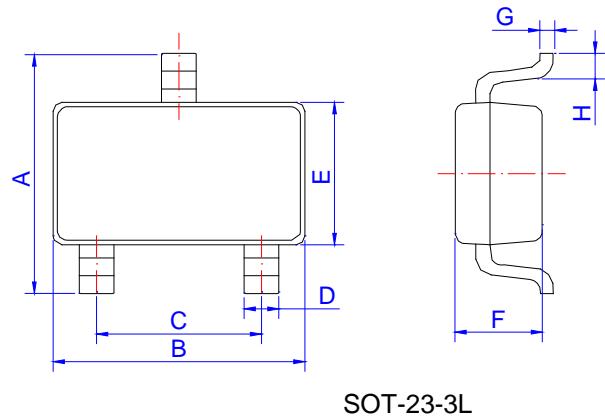
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(μA)	Package	Base qty. (pcs)	Delivery mode
100-8	800	≤200	SOT-23-3L	3,000	Tape & Reel

Document Revision History

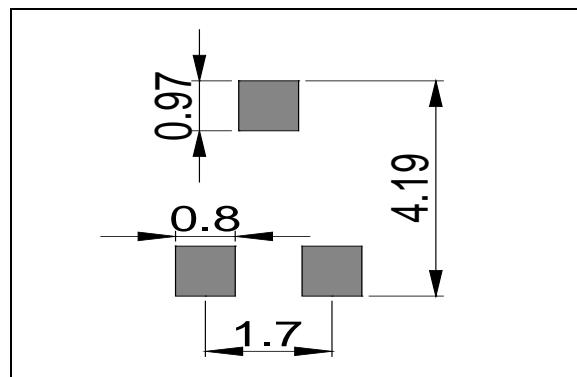
Date	Revision	Changes
May.16, 2023	A.1.0	Last update

PACKAGE MECHANICAL DATA

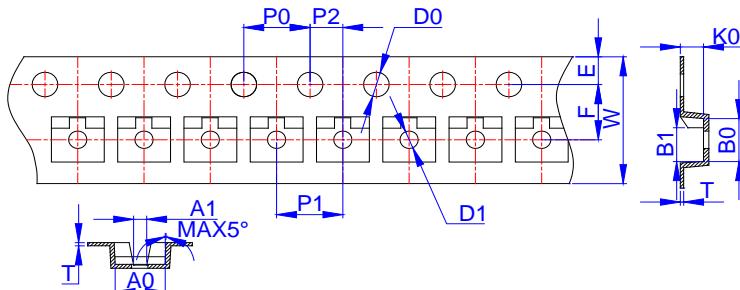


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.65	2.80	2.95	0.104	0.110	0.116
B	2.82	2.92	3.02	0.111	0.115	0.119
C	1.80	1.90	2.00	0.071	0.075	0.079
D	0.30	0.35	0.50	0.012	0.014	0.020
E	1.50	1.60	1.70	0.059	0.063	0.067
F	1.07	1.17	1.27	0.042	0.046	0.050
G	0.05	0.15	0.25	0.002	0.006	0.010
H	0.25	0.40	0.55	0.010	0.016	0.022

FOOTPRINT-SOT-23-3L (dimensions in mm)



DELIVERY MODE



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A0	3.10	3.20	3.30	0.122	0.126	0.130
A1	1.02	1.04	1.06	0.040	0.041	0.042
B0	3.18	3.28	3.38	0.125	0.129	0.133
B1	2.39	2.49	2.59	0.094	0.098	0.102
K0	1.22	1.32	1.42	0.048	0.052	0.056
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	3.90	4.00	4.10	0.154	0.157	0.161
P2	1.95	2.00	2.05	0.077	0.079	0.081
T	0.15	0.20	0.25	0.006	0.008	0.010
E	1.65	1.75	1.85	0.065	0.069	0.073
F	3.45	3.50	3.55	0.136	0.138	0.140
D0	1.50	1.55	1.60	0.059	0.061	0.063
D1	1.00	1.10	1.20	0.039	0.043	0.047
W	7.90	8.00	8.20	0.311	0.315	0.323

PACKAGE	OUTLINE	REEL (PCS)	PER CARTON (PCS)	TAPE & REEL
SOT-23-3L	TAPING	3,000	120,000	7 inch



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents.

Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2023 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.